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Rambus Advances Its Smart Data Acceleration Research Program by Partnering With Los Alamos National Laboratory

Initial results show increased performance for server and data center workloads

SUNNYVALE, Calif.--(BUSINESS WIRE)-- [Rambus Inc.](#) (NASDAQ: RMBS) today announced that it has partnered with [Los Alamos National Laboratory](#) (LANL) for evaluating elements of its Smart Data Acceleration (SDA) Research Program. The SDA platform has been deployed at LANL to improve the performance of in-memory databases, graph analytics and other Big Data applications.

"With the advent of new tiers of the memory hierarchy coupled with high-speed modern networks that are being made available to the HPC community, independent scaling of memory resources in different tiers may be possible, which will help economically match the needs of future applications and workflows of extreme high-end HPC," said Gary Grider, leader of the High Performance Computing Division, Los Alamos National Laboratory. "Our initial tests of the Rambus SDA platform show that its performance is well-matched to HPC interconnects, and given the ability to process information close to memory, it can enable the reduction of data movement through efficient workflows. The SDA Research platform allows for large scale simulation by combining independently scalable, high-performance, high-durability, large capacity and processing near memory."

Addressing major issues facing servers and data centers in the age of Big Data, the SDA Research Program targets significant improvements in the acceleration and offload of computation as the industry explores new paradigms such as near data processing. In addition to compute offload and acceleration with flexible processing engines to enhance system performance, key focus areas include minimizing data movement and leveraging the improved bandwidth, and reducing latency of DRAM and other memory technologies.

"LANL is an ideal partner for our SDA Research Program as we collaborate with experts to validate our approach and overall direction," said Laura Stark, senior vice president and general manager of the Emerging Solutions division at Rambus. "Our goal is to provide significant improvements in the performance of data-intensive applications through optimization of both software and hardware for compute acceleration and offload."

The Rambus SDA Research Program provides a platform to investigate near data processing system architectures that include software, firmware, FPGAs and large amounts of memory. The platform can be used to test new methods to optimize and accelerate analytics for large data sets, including in-memory data bases, financial services, ad serving, real-time risk analytics, imaging, transcoding and genome mapping among other applications.

More details about the Rambus SDA Research program can be found at rambus.com/sda.

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About Emerging Solutions Division

Inspired by the innovative thinking at the heart of Rambus Labs, the Emerging Solutions division at Rambus works to translate extraordinary theory into everyday practice, imagining exciting new ways to interpret and reimagine the world of data around us.

About Rambus Inc.

Rambus creates cutting-edge semiconductor and IP products, spanning memory and interfaces to security, smart sensors and lighting. Our chips, customizable IP cores, architecture licenses, tools, services, training and innovations improve the competitive advantage of our customers. We collaborate with the industry, partnering with leading ASIC and SoC designers,

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