



Paul Kocher to Discuss Future of Security at Intel Developer Forum 2011

SAN FRANCISCO--(BUSINESS WIRE)-- Rambus Inc. (NASDAQ:RMBS):

Who: Rambus Inc. (NASDAQ:RMBS)

Where: 2011 Intel Developer Forum
Moscone West
San Francisco, CA

When: September 13-15, 2011

Join Rambus, one of the world's premier technology licensing companies, at the Intel Developer Forum 2011 to hear from Cryptography Research's Paul Kocher about the future of semiconductor security. In addition, Rambus will exhibit demos of its innovations from its Terabyte Bandwidth Initiative, Mobile XDR memory architecture, DDR3 memory interface and Cryptography Research.

Rambus Presentation:

The Future of Security is in Silicon

Paul Kocher, president and chief scientist at Cryptography Research, Inc., a division of Rambus
Wednesday, September 14, 2011, 11:20am PT, Room 2006

Hardware-level security provides a key component of a comprehensive security solution. In this talk, Paul Kocher will discuss architectures and approaches that can provide strong and cost-effective security in silicon.

Rambus Demos:

Terabyte Bandwidth Initiative

The latest technology advancements of Rambus' Terabyte Bandwidth Initiative enable unmatched power efficiency and compatibility to single-ended memory architectures, including GDDR5 and DDR3. This demo will showcase how Rambus' Terabyte Bandwidth Initiative has achieved a power efficiency of 6 milliwatts per gigabit per second (mW/Gbps) when operating at 20Gbps in a 40nm-process silicon test vehicle.

DDR3 Controller Interface Solution

Optimized for consumer applications, this high-performance DDR3 interface solution is implemented in a low-cost wire bond package. It performs memory transactions to and from a DDR3 SDRAM device operating at 1866MT/s with architectural support for data rates of up to 2133MT/s.

Mobile XDR™ Memory Architecture

The Mobile XDR™ memory architecture is the world's fastest and most power efficient memory for mobile applications. The demonstration features the Mobile XDR memory interface implemented in a package-on-package configuration signaling at 4.3Gbps with a power efficiency of 2.2mW/Gbps.

Cryptography Research Inc.

A world-leader in semiconductor security, Cryptography Research will provide a demonstration using its DPA Workstation™ to showcase foundational technologies that protect systems and chips against power analysis attacks.

About Rambus Inc.

Rambus is one of the world's premier technology licensing companies. Founded in 1990, the Company specializes in the invention and design of architectures focused on enriching the end-user experience of electronic systems. Rambus' patented innovations and breakthrough technologies help industry-leading companies bring superior products to market. Rambus licenses both its world-class patent portfolio, as well as its family of leadership and industry-standard solutions. Rambus has offices in California, North Carolina, Ohio, India, Germany, Japan, Korea, and Taiwan. Additional information is available at www.rambus.com.

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