



## Rambus Engineers Author Book on Signal Integrity

*New book provides comprehensive overview of high-speed signaling for electronic system design*

SUNNYVALE, Calif.--(BUSINESS WIRE)-- Rambus Inc. (NASDAQ: RMBS), one of the world's premier technology licensing companies, today announced that two of its engineers have authored a book titled "High-Speed Signaling: Jitter Modeling, Analysis, and Budgeting." As the speed of digital electronics increases, signal integrity is a critical issue that circuit and system design engineers must address. Signal integrity is the quality of an electrical signal as it travels between integrated circuits in an electronic system. This book offers key insights for technologists interested in state-of-the-art techniques for achieving the signal and power integrity required in today's high-performance electronics.

"Rambus has a long history of innovation in many areas with a particular emphasis on high-speed, mixed-signal digital circuit and interface design in electronic systems," said John P. Kent, vice president of Technology Development at Rambus. "As electronic devices push to gigabit data rates and beyond, understanding the methods of maintaining signal and power integrity is vital. By showcasing our collection of knowledge in this field, we can help advance the development of the next generation of electronic products."

The authors, Dan Oh and Chuck Yuan along with several Rambus contributors, have spent years researching signal integrity and have published more than 100 papers on the subject. This book summarizes the current status of this evolving field and describes the latest advances in signal and power integrity. It offers circuit and system engineers techniques for the design of robust and efficient interface systems.

"High-Speed Signaling: Jitter Modeling, Analysis, and Budgeting" has been published by Prentice Hall and is available for purchase on Amazon.com at the following [link](#).

For additional information on the "High-Speed Signaling: Jitter Modeling, Analysis, and Budgeting" book or on Rambus' technology, visit [www.rambus.com/signalintegrity](http://www.rambus.com/signalintegrity).

### About the Authors

**Dan Oh** is a senior principal engineer at Rambus Inc. He holds a Ph.D. in electrical engineering from the University of Illinois, Urbana-Champaign. Dr. Oh has published more than 80 papers and holds seven U.S. issued patents and 10 pending patent applications in areas of high-speed link design. He serves on the technical program committee of IEEE EPEPS, and is a former member of the IEC DesignCon Technical Program Committee.

**Chuck Yuan** is an engineering director at Rambus. He holds a Ph.D. in electrical engineering from Syracuse University. Dr. Yuan holds eight U.S. issued patents and has been published in over 100 journal and conference publications. Prior to joining Rambus, he worked as a senior consulting engineer at Cadence Design Systems.

### About Rambus Inc.

Rambus is one of the world's premier technology licensing companies. Founded in 1990, the Company specializes in the invention and design of architectures focused on enriching the end-user experience of electronic systems. Rambus' patented innovations and breakthrough technologies help industry-leading companies bring superior products to market. Rambus licenses both its world-class patent portfolio, as well as its family of leadership and industry-standard solutions. Headquartered in Sunnyvale, California, Rambus has regional offices in North Carolina, Ohio, India, Germany, Japan, Korea, and Taiwan. Additional information is available at [www.rambus.com](http://www.rambus.com).

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