



## Rambus to Present Seven Technical Papers on Advances in Signal Integrity and High-Speed Design at EPEPS 2011

SAN JOSE, Calif.--(BUSINESS WIRE)-- Rambus Inc. (NASDAQ:RMBS):

**Who:** Rambus Inc. (NASDAQ:RMBS)

**Where:** EPEPS 2011  
Doubletree Hotel  
San Jose, CA

**When:** October 23-26, 2011

Join Rambus, one of the world's premier technology licensing companies, at the Electrical Performance of Electronic Packaging and Systems (EPEPS) 2011 Conference in San Jose, CA. Rambus engineers will be presenting seven technical papers on topics ranging from high-speed design, signal integrity, jitter modeling and low-power clocking technology, among others. The papers will reveal progress and the latest technology developments in the field of high-speed circuit design.

### Rambus Papers:

#### Session MV-IV.1

**Title:** Plane Bounce in High-speed Single-ended Signaling I/O Interfaces

Dan Oh, Rambus

Monday, October 24, 2011, 9:00 a.m.

Room: Donner/Siskiyou

#### Session M-XI

**Title:** Jitter Modeling and Analysis

Wendem Beyene, Rambus (Chair)

Monday, October 24, 2011, 3:50 p.m.

Room: Donner/Siskiyou

#### Session M-XI.2

**Title:** Extraction of Jitter Parameters from BER Measurements

Marko Aleksic, Rambus

Monday, October 24, 2011, 3:50 p.m.

Room: Donner/Siskiyou

#### Session M-XII.9

**Title:** A New Method to Estimate Phases of Sinusoidal Jitter to Evaluate High-Speed Links

Yu Chang, Chris Madden, and Ralf Schmitt, Rambus

Monday, October 24, 2011, 4:30 p.m.

Room: Cascade/Sierra

#### Session M-XII.18

**Title:** Application of the Latency Insertion Method to Electro-Thermal Circuit Analysis

Wendem Beyene, Don Mullen, Ming Li, Ralf Schmitt, and Ling Yang, Rambus

Monday, October 24, 2011, 4:30 p.m.

Room: Cascade/Sierra

#### Session T-X.1

**Title:** Design and Analysis of 12.8Gb/s Single-Ended Signaling for Memory Interface

Wendem Beyene, Amir Amirkhany, Chris Madden, Hai Lan, Ling Yang, Kambiz Kaviani, Sanku Mukherjee, Dave Secker, and Ralf Schmitt, Rambus

Tuesday, October 25, 2011, 3:55 p.m.

Room: Donner/Siskiyou

### **Session T-X.3**

**Title:** System Design Considerations for a 5Gb/s Source-Synchronous Link with Common-mode Clocking

Jihong Ren, Dan Oh, Ravi Kollipara, Brian Tsang, Yue Lu, and Jared Zerbe, Rambus

Tuesday, October 25, 2011, 3:55 p.m.

Room: Donner/Siskiyou

### **About Rambus Inc.**

Rambus is one of the world's premier technology licensing companies. Founded in 1990, the Company specializes in the invention and design of architectures focused on enriching the end-user experience of electronic systems. Rambus' patented innovations and breakthrough technologies help industry-leading companies bring superior products to market. Rambus licenses both its world-class patent portfolio, as well as its family of leadership and industry-standard solutions. Headquartered in Sunnyvale, California, Rambus has regional offices in North Carolina, Ohio, India, Germany, Japan, Korea, and Taiwan. Additional information is available at [www.rambus.com](http://www.rambus.com).

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