October 7, 2008

Rambus to Outline Memory Trends in High-Definition Consumer Devices at ARM Developers' Conference

Oct 07, 2008 (BUSINESS WIRE) -- Rambus Inc. (Nasdaq:RMBS)

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Who:        Rambus Inc. (Nasdaq: RMBS)
Where:      ARM Developers' Conference 2008
            Santa Clara Convention Center
            5001 Great America Parkway
            Santa Clara, CA
            Room #203
When:       Thursday, October 9, 2008, 9:00 am
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Join Rambus at the ARM Developers' Conference 2008 as Director of Marketing, Michael Ching, outlines the memory architecture trends and system bandwidth requirements for designing SoCs for high-definition consumer applications.

During this presentation, Mr. Ching will analyze DTV, Blu-Ray Disc Players and high-definition set-top box chip architectures that combine ARM video decoders, processing CODECs, processors and graphics cores interconnected through an AXI switch fabric. Memory subsystems using XDR(TM) DRAM, DDR2 and DDR3 DRAMs will be compared for bandwidth efficiency, controller interface pins, component count, and bill of material.

About Rambus Inc.

Rambus is one of the world's premier technology licensing companies specializing in the invention and design of high-speed memory architectures. Since its founding in 1990, the Company's patented innovations, breakthrough technologies and renowned integration expertise have helped industry-leading chip and system companies bring superior products to market. Rambus' innovations and solutions enable unprecedented performance in computing, communications, and consumer electronics applications. Rambus licenses both its world-class patent portfolio as well as a range of leadership and industry-standard memory solutions. Headquartered in Los Altos, California, Rambus has regional offices in North Carolina, India, Germany, Japan, and Taiwan. Additional information is available at www.rambus.com.

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