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Rambus Launches JEDEC-Standard DDR4 NVRCD for Emerging NVDIMM Applications

Enables high performance and reliability of data for next-generation cloud data centers

SUNNYVALE, Calif.--(BUSINESS WIRE)-- [Rambus Inc.](#) (NASDAQ:RMBS) today announced the industry's first production release of a JEDEC-standard persistent memory register clock driver (NVRCD) for use with DDR4 non-volatile dual in-line memory modules (NVDIMM). Operating at data rates up to 3200 MT/s, the [Rambus DDR4 NVRCD](#) enables NVDIMMs to combine the performance of DRAM, with the persistence of storage-class memory to meet the needs of data intensive workloads like high-performance file systems, databases, big data analytics and storage.

NVDIMMs bridge the gap between dynamic random access memory (DRAM) and storage, allowing greater flexibility in data management by providing non-volatile, low-latency memory closer to the processor. According to analyst forecasts, demand for NVDIMM technologies is set to increase from fewer than five million units in 2017 to more than 25 million units in 2020.

"The pairing of non-volatile memory with DRAM in the same module keeps the data near processing and drastically reduces latency and increases performance," said Luc Seraphin, senior vice president and general manager of the Rambus Memory and Interfaces Division. "As the latest addition to our server DIMM chipset family, our DDR4 NVRCD builds upon our existing high-speed chip and signalling expertise to deliver differentiated, persistent memory solutions ideal for high-performance, high-capacity enterprise and data center systems."

Fully compliant with the latest JEDEC DDR4 RCD and NVRCD specifications, the Rambus DDR4 NVRCD delivers industry-leading I/O performance and margins. It is the first chip in production for DDR4 NVDIMM-N products and paves the way for emerging NVDIMM-P architectures. The NVRCD also supports NV-RDIMMs and NV-LRDIMMs operating at speeds up to DDR4-3200.

The NVRCD is available in production quantities today.

For additional information on Rambus Server DIMM chips, please visit: rambus.com/dimmchipset

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About Rambus Memory and Interfaces Division

The Rambus Memory and Interfaces Division develops products and services that solve the power, performance, and capacity challenges of the communications and data center computing markets. Rambus enhanced standards-compatible and custom memory and serial link solutions include chips, architectures, memory and SerDes interfaces, IP validation tools, and system and IC design services. Developed through our system-aware design methodology, Rambus products deliver improved time-to-market and first-time-right quality.

About Rambus Inc.

Rambus creates innovative hardware and software technologies, driving advancements from the data center to the mobile edge. Our chips, customizable IP cores, architecture licenses, tools, software, services, training and innovations improve the competitive advantage of our customers. We collaborate with the industry, partnering with leading ASIC and SoC designers, foundries, IP developers, EDA companies and validation labs. Our products are integrated into tens of billions of devices and systems, powering and securing diverse applications, including Big Data, Internet of Things (IoT), mobile payments, and smart ticketing. At Rambus, we are makers of better. For more information, visit rambus.com.

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